

Title: Integrated Circuit Verification Scheme Application No: 10/645,789 File No: ADAPP243 Inventors: Mohanty et al. Page 1 of 5

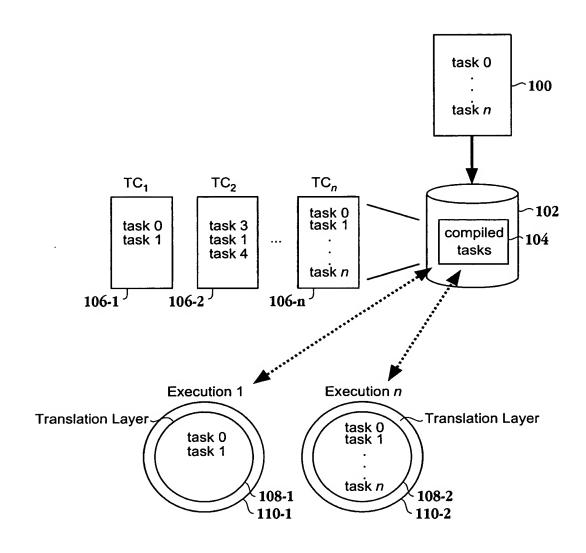


Fig.

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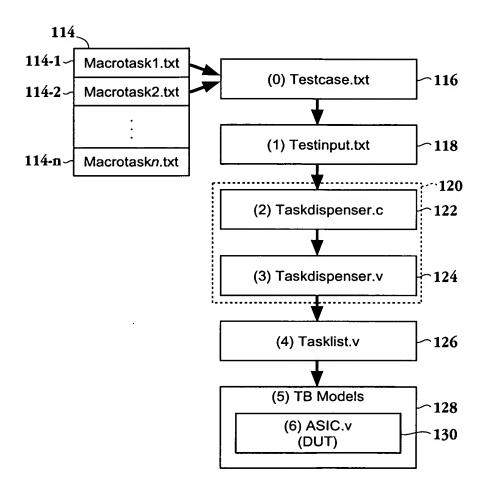


Fig. 2

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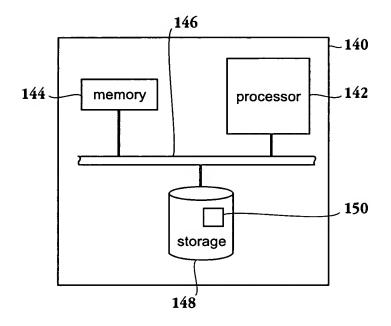


Fig. 3

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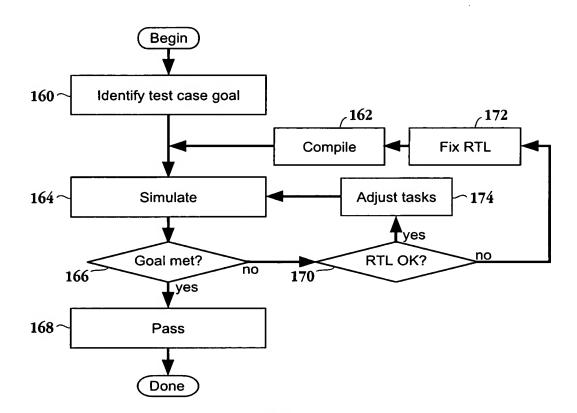


Fig. 4

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